IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

John W. Rapp, Larry Jackson, Mark Jones, and Troy Cherasaro

PIPELINE ACCELERATOR FOR IMPROVED COMPUTING

ARCHITECTURE AND RELATED SYSTEM AND METHOD

SEP 1 9 2005 Serial Number: 10/683,929

Applicants:

Title:

Filing Date: October 9, 2003

Examiner/Unit: / 2818

Attorney Docket No.: 1934-13-3

CERTIFICATE OF MAILING OR TRANSMISSION

I hereby certify that this correspondence is being deposited in the United States Postal Service as First Class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 16th day of September, 2005.

RULE 56 INFORMATION DISCLOSURE AND ACCOMPANYING SECTION 1.97(e) CERTIFICATION

TO THE COMMISSIONER FOR PATENTS:

In compliance with 37 CFR § 1.56 and § 1.97, Applicants acting by and through the attorney of record, hereby discloses patents and other documents listed on the attached Form PTO-1449 which may be considered relevant to the examination of the subject application.

Copies of the cited non-U.S. references are enclosed.

Certification Under Section 1.97(e)

Each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign Patent Office in a counterpart foreign application not more than three months prior to the filing of this statement.

Respectfully submitted,

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1934-13-3

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Attorney Docket Number

	U.S. PATENT DOCUMENTS							
Examiner	Cita	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant			
Initials *	Cite No. ¹	Number - Kind Code ² (if known)		Cited Document	Passages or Relevant Figures Appear			
		US-5,892,962	04/06/99	Cloutier				
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FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶	
	No.1	Country Code ³ - Number ⁴ - Kind Code ⁵ (<i>if known</i>)					
		EP 1 061 439 A1	12/20/00	Hewlett- Packard Company			
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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450, DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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		D10	01.001105	Application Number	10/683,929	
			CLOSURE	Filing Date	October 9, 2003	
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				Art Unit	2818	
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NON PATENT LITERATURE DOCUMENTS					
Examiner Cite No.1		Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			
		SALCIC Z ET AL., "FLIX environment for generation of custom-configurable machines in FPLDs for embedded applications", Microprocessors and Microsystems, IPC Business Press Ltd. London, GB, vol. 23, no. 8-9, December 15, 1999, pp. 513-526			
		International Search Report for PCT/US 03/34558, June 16, 2005			
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